

ABSTRACT OF THE DISCLOSURE

A circuit for processing charge detecting signals transferred to a floating diffusion amplifier from a charge coupled device includes a first node connected to the floating diffusion amplifier; a first enhancement type FET connected in series between a first fixed-voltage supply line for supplying a first fixed voltage and an output terminal, where the first enhancement type FET has a first gate connected to the first node; and a second enhancement type FET connected in series between a second fixed-voltage supply line for supplying a second fixed voltage and the output terminal, where the second enhancement type FET has a second gate supplied with a third fixed voltage which is different in potential from the second fixed voltage.